

09/733961

1 ABSTRACT OF THE DISCLOSURE

 A semiconductor device which receives
addresses in synchronism with a clock signal and
receives data in synchronism with a strobe signal
5 includes address-latch circuits, a first control
circuit which selects one of the address-latch
circuits in sequence in response to the clock signal,
and controls the selected one of the address-latch
circuits to latch a corresponding one of the addresses
10 in response to the clock signal, and a second control
circuit which selects one of the address-latch
circuits in sequence in response to the strobe signal,
and controls the selected one of the address-latch
circuits to output a corresponding one of the
15 addresses in response to the strobe signal.

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